

[54] CHARGE-COUPLED DEVICE OUTPUT CIRCUIT

[75] Inventors: Tetsuya Iida; Tatsuo Sakaue, both of Yokohama, Japan

[73] Assignee: Tokyo Shibaura Denki Kabushiki Kaisha, Kawasaki, Japan

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Primary Examiner—John S. Heyman

Attorney, Agent, or Firm—Schwartz, Jeffery, Schwaab, Mack, Blumenthal & Evans

[57] ABSTRACT

A circuit for obtaining an output from a CCD type signal processing circuit should operate satisfactorily in a high frequency band covering, for instance, video signals. For this purpose, the floating gate electrode of the CCD is connected to a first potential point through first and second capacitors. The connecting point of the floating gate electrode in the series circuit is connected to a second potential level through a first control switch which is driven by a reset clock pulse, and the connecting point of the first and second capacitors is connected to a third potential level through a second control switch which is driven by a reset clock pulse, so that the output is provided at the connecting point of the capacitors.

4 Claims, 9 Drawing Figures

